# Changes to Commands and Properties

## Command Changes

|  |  |
| --- | --- |
| **Property Name** | **Comment** |
| add\_regbus\_controller | This command adds a Regbus Controller (RBC) with its RBM and optional Axi4s bridge to tunnel requests from masters in the NoC, or APBM| AXI4LM| IOSFSB| IOSFSB2APB interfaces to drive external requests into the RBC. |
| vifce\_prop | This command is used to set or view a named property of a particular Interface's virtual interface and an optional direction in/out, w.r.t the host. |
| reset\_mesh | This command resets the previously created mesh. |

## Default Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| rssb\_router | no | Deprecated. By default all routers are RSSB. |

## Mesh Property Changes

None

## Bridge Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| axi4m\_ar\_max\_beats | 256 | This property is used to set the limit on the burst length for reads. |
| axi4m\_aw\_max\_beats | 256 | This property is used to set the limit on the burst length for writes. |
| axi4s\_memory\_type | device | This property indicates the Memory Type fot the AXI slave as described in the AXI spec. |
| cmi\_rd\_req\_max\_credit\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| cmi\_rdcpl\_max\_credit\_error\_fatal | no | This property is used to control whether the interrupt raise on read credit overflow will be fatal or non-fatal. |
| cmi\_route\_lookup\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| cmi\_rsp\_max\_credit\_error\_fatal | no | This property is control whether the interrupt raise on read credit overflow will be fatal or non-fatal. |
| cmi\_unsupported\_opcode\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |

## Host Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| llc\_data\_ram\_latency | 2 | This property specifies the latency of the data array for this cache. |
| llc\_tag\_ram\_latency | 2 | This property specifies the latency of the tag array for this cache. |

## Interface Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| cmi\_supports\_interleaved\_response | no | For CMIM bridges, indicates whether the CMI VC can accept interleaved read data completion responses. |

## Link Property Changes

None

## Router Property Changes

None

## VC Property Changes

None

## CSB Storage Property Changes

None